

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Patent Application of:

Hyun-kyu YUN at al.

Application 10/808,341 Group Art Unit: 2189

Filed: March 25, 2004 Examiner Woo H. Choi

Customer No.: 38209 Confirmation No. 5441

For: DSP (DIGITAL SIGNAL PROCESSING) ARCHITECTURE WITH A WIDE  
MEMORY BANDWIDTH AND A MEMORY MAPPING METHOD THEREOF

**BOX AF**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**AMENDMENT AFTER FINAL REJECTION**

*M.L.*

Sir:

In response to the Final Office Action mailed March 21, 2007, please amend the above-identified application as follows:

*Entered with  
RCT  
6-20-07*

**Amendments to the Claims** are reflected in the listing of claims which begins on page 2 of this paper.

**Remarks** begin on page 11 of this paper.